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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/606,863	06/27/2003	Yosuke Okitsu	11-165	5832
23400	7590	10/05/2005	EXAMINER	
POSZ LAW GROUP, PLC 12040 SOUTH LAKES DRIVE SUITE 101 RESTON, VA 20191			PARRIES, DRU M	
			ART UNIT	PAPER NUMBER
			2836	

DATE MAILED: 10/05/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/606,863

Applicant(s)

OKITSU ET AL.

Examiner

Dru M. Parries

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 June 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-6 is/are rejected.
- 7) ☒ Claim(s) 7-9 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 27 June 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 6-27-03.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Information Disclosure Statement

1. The information disclosure statement (IDS) submitted on June 27, 2003 is in compliance with the provisions of 37 CFR 1.97, and accordingly, has been considered by the examiner.

Claim Objections

2. Claims 3 and 5 objected to because of the following informalities: Both claims reference the "control apparatus" when the Examiner believes the reference should be made to the "drive apparatus" (1st indent of claim 3; last indent of claim 5). Also, in regards to claim 5, there is a misspelled word in the indentation talking about the "waiting status detection means" (the word "aid"). Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the

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reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

4. Claims 1 and 2 are rejected under 35 U.S.C. 102(e) as being anticipated by Mayama et al. (6,778,001). Mayama teaches a control apparatus (+B, SW) that produces a power supply enabling control signal, as a drive control signal, to a signal input point (24) in said drive apparatus (14, 16). He also teaches a drive apparatus that outputs a drive signal (143), in accordance with the drive control signal, which controls driving of a load (L), and that receives the power supply enabling control signal and is adapted to set an internal circuit thereof in a conducting condition to enable electrical power from a drive power source (+B) to the drive apparatus and sets the internal circuit in a non-conducting state when the power supply enabling control signal ceases to be supplied. (Fig. 1)

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 3 and 4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Mayama et al. (6,778,001) and Freymuth (4,636,711). Mayama teaches a load drive control system as described above. Mayama also teaches the input signal applied at node 24 will be either +B (when SW is ON) or 0V (when off). He teaches a high-side switch (SW) which is coupled to the signal input point (24) and an internal DC power supply (+B) for producing a supply voltage at a

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predetermined voltage level. Mayama also teaches a first switching element (161) coupled to an integrator circuit (162, 163, 164) for being set in a conducting condition when the integrated voltage is derived and non-conducting when voltage is terminated. Mayama teaches a second switching element (12) being controlled, indirectly, by the first switching element for supplying electrical power from the drive power source (+B) to a part of the drive signal output section (L), and interrupting supplying power while first switch is not conducting. Mayama doesn't teach generating a drive control signal by Pulse Width Modulation. Freymuth teaches driving a switch (114) by initially producing a Pulse Width Modulated signal from the control apparatus (10) (Fig. 1 and 1E). It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate Pulse Width Modulation into Mayama's control apparatus so that the power supplied to the load is more easily controlled on the basis of the control signal formed as a pulse signal by changing the duty cycle of the pulse.

7. Claims 5 and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prior Art (Admission), Mayama et al. (6,778,001) and Freymuth (4,636,711). Admission teaches a low-side switching element (10) coupled to the input of a drive apparatus (2). Admission teaches a drive signal output section (3, 4), which outputs a drive signal to the load, and adapted to operate on an input signal (a drive control signal), which varied between ground potential, and a first predetermined voltage level (V_{IN}). It also teaches waiting status detection means (4) for generating an internal control signal. Admission fails to teach the method of connecting and disconnecting said signal input point to and from ground, nor does it teach the enabling and interrupting of a DC power source to the drive signal output section. Admission also fails to teach signal level lowering means for setting a maximum level of the input signal. Mayama

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teaches a power supply enabling control section (16) controlled by the control signal (at 24) for enabling or interrupting a DC supply at a second level to part of the drive signal output section (14 via switch 161) depending on whether the drive control signal is terminated or not. Mayama also teaches a signal level lowering means (164) for setting a maximum level of the input signal (Col. 9, lines 8-23). It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate an enabling/disabling means and a voltage limiter in the drive apparatus so that power won't be supplied at all times and therefore save power. Freymuth teaches driving a switch (114) by initially producing a Pulse Width Modulated signal from the control apparatus (10) (Fig. 1 and 1E). It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate a pulse width modulated signal into the Prior Art's input signal (i.e. connecting and disconnecting the switch, 10) so that the power supplied to the load is more easily controlled on the basis of the control signal formed as a pulse signal by changing the duty cycle of the pulse.

Allowable Subject Matter

8. Claims 7-9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Claim 7 would be allowable because the prior art fails to teach the drive apparatus comprising a third switching element, and a third switching element with a zener diode connected in that specific configuration, and since claims 8 and 9 are dependant on claim 7, they would be allowable also.

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Conclusion


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dru M. Parries whose telephone number is (571) 272-8542. The examiner can normally be reached on Monday -Thursday from 8:00am to 5:00pm. The examiner can also be reached on alternate Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus, can be reached on 571-272-2800 x 36. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DMP

9-23-2005



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